

Diagrams

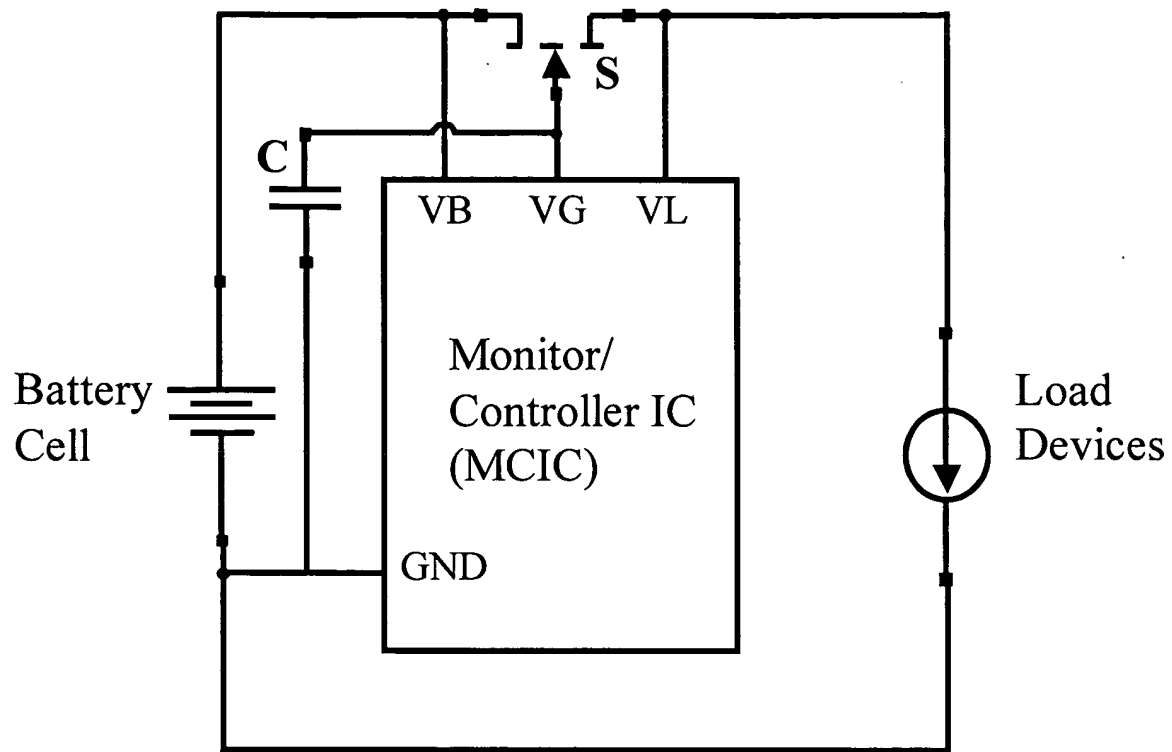


Fig. 1: One possible system-level embodiment of the invention

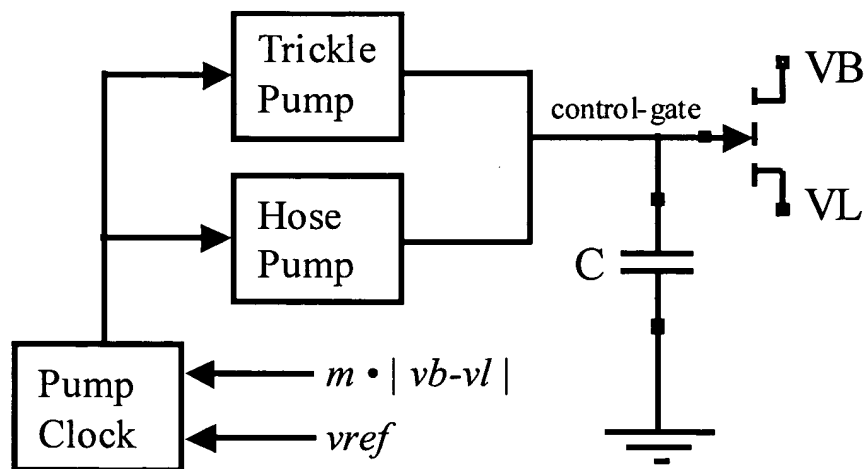


Fig. 2: An illustration of Variable-Current Drive and Dual-Function Capacitor

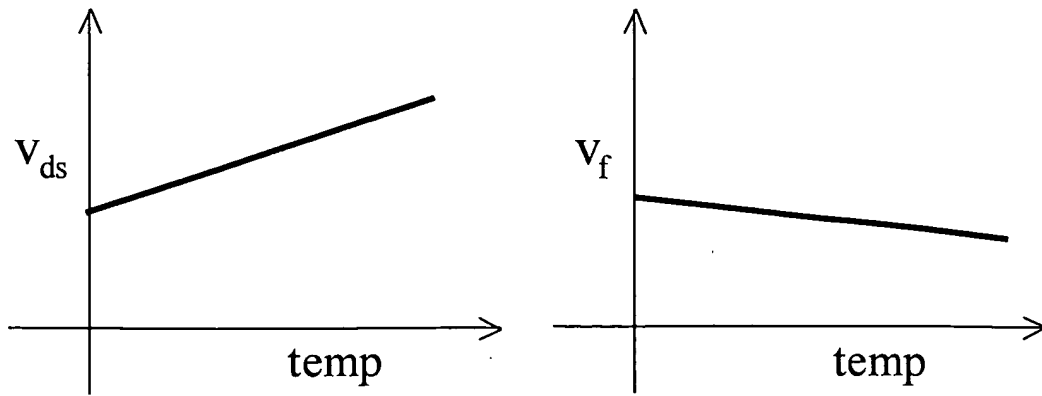


Fig. 3: Illustrations of channel and gate junction voltage drops of the switch